

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

van der GOES *et al.*

Appl. No. *To Be Assigned*

Filed: Herewith (July 24, 2003)

For: **Subranging Analog to Digital
Converter with Multi-Phase Clock
Timing**

Art Unit: *To Be Assigned*

Examiner: *To Be Assigned*

Atty. Docket: 1875.2820002/RES/GSB

Request to Approve Proposed Drawing Corrections

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

Attached is a copy of three (3) sheets of drawings, containing proposed correction to FIG. 4 (with shading removed) and new FIGs. 10 and 11. The proposed change adds no new matter to this application. Applicants request that the Examiner approve the proposed correction.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.



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Attorney for Applicants
Registration No. 44,757

Date: 7/24/03

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NOTICE OF FEE DUE

DATE: 7-25-3

TO: _____

FROM: Office of Initial Patent Examination

SUBJECT: Fee Due

APPLICATION NUMBER: 10625207

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If you have any questions, please contact Cynthia Streater at 703-306-5430 or Eleanor Kurtz at 703-308-3642.

Terminal Operator _____